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U.S. PRO
1/29/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NO.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10059940	01/29/2002	219	72	1725	Elve

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**CONTINUING DATA VERIFIED:

**FOREIGN APPLICATIONS VERIFIED:

SINGAPORE 200106032-6 10/01/2001

3 sheets of formal Drawings 1-29-02

FC-102 DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO.
1 claim(s) fully claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No		100-10059940/JPW/MS
1 U.S. 113 conditions met <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		
1 David A. [Signature] indicated Examiner's initials		
1. Method and apparatus for deflashing of integrated circuit packages		
15 U.S.C. § 1071 et seq.; 35 U.S.C. § 1071 et seq.; 37 CFR 1.10 et seq.		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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